

REMARKS

The Examiner is thanked for the thorough review and consideration of the present application, as well as for the courtesies extended to Applicants' representative during the telephonic interviews conducted on August 25, 2003 and August 26, 2003. The non-final Office Action dated July 7, 2003 has been received and its contents carefully reviewed.

By this Response, Applicants have amended the specification to correct a minor typographical error. Also, claims 1, 12, 14-15 and 20-21 have been amended to clarify the subject matter of the invention. Claims 1-7, 9-18 and 20-21 are pending in the application. No new matter has been added. Reconsideration and withdrawal of the rejections in view of the above amendments and the following remarks are requested.

In the Office Action, claims 1-5, 9-16 and 19-21 are rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,909,035, issued to Kim. Claims 1-5, 9-12 and 14-17 are rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,025,835, issued to Aoki, et al. ("Aoki"). And, claims 6 and 7 are rejected under 35 U.S.C. § 103(a) as being unpatentable over Kim. Applicants traverse the rejections because neither Kim nor Aoki, analyzed alone or in any combination, teach or suggest each of the features recited in the claims of the present application. For example, Kim and Aoki fail to teach or suggest a liquid crystal display device having among other features, "a first auxiliary line arranged in the non-display area and applying a voltage to the liquid crystal layer...and a second auxiliary line arranged in the non-display area", as recited in amended claims 1, 12, 14, 15, 20 and 21 of the present application. Applicants respectfully note that claim 19 was previously cancelled. Thus, the rejection as applied to cancelled claim 19 is rendered moot.

Kim discloses a thin film transistor array in a liquid crystal panel having “a first static electricity prevention circuit associated with each scanning line, each first static electricity prevention circuit being connected between the associated scanning line and the gate short line; and a second static electricity prevention circuit associated with each data line, and each second static electricity prevention circuit being connected between the associated data line and the data short line” (col. 5, lines 4-11).

Aoki discloses “a display apparatus a driving circuit for driving a display apparatus, comprising a first common line and a second common line to which picture signals are input, a first switch device group connected to the first common line and adapted for sampling a picture signal, a second switch device group connected to the second common line and adapted for sampling a picture signal, a timing generating circuit for controlling switching operations for the first switch device group and the second switch device group, a first output enable means in common with the first switch device group, a second output enable means in common with the second switch device group, and a plurality of display signal lines disposed corresponding to each of pairs of the first switch devices and the second switch devices and a plurality of pixels connected thereto, wherein a common control signal is input from the timing generating circuit to a pair of switch devices having a first switch device and a second switch device, a picture signal being output from a switch device enabled by the first output enable means or the second enable means” (col. 2, lines 39-58).

However, neither Kim nor Aoki teach or suggest “a first auxiliary line arranged in the non-display area and applying a voltage to the liquid crystal layer...and a second auxiliary line arranged in the non-display area”, as recited in claims 1, 12, 14, 15, 20 and 21 of the present

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application. Since Kim and Aoki fail to teach or suggest each of the features recited in independent claims 1, 12, 14, 15, 20 and 21, claim 1 and its dependent claims 2-7, 9-10, 11 and 13, claim 12, claim 14 and its dependent claims 16-18, and claims 15, 20 and 21 are patentable over Kim and Aoki. Reconsideration and withdrawal of the rejections are requested.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue. If the Examiner deems that a telephone conversation would further the prosecution of this application, the Examiner is invited to call the undersigned at (202) 496-7500.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911. A duplicate copy of this sheet is enclosed

Dated: September 30, 2003

Respectfully submitted,

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